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Roza Kotlyar received the B.S. degree in physics from the Polytechnic University, Brooklyn, NY, in 1992 and the Ph.D. in theoretical condensed matter physics from the University of Maryland, College Park, in 1998. Her thesis advisor was Professor Sankar Das Sarma. From 1998 to 2000, she was a National Research Council Postdoctoral Fellow with the Electronics Science and Technology Division (ESTD), Naval Research Laboratory, Washington, DC. Since 2000, she has been with the Process Technology Modeling Department, Intel Corporation, Hillsboro, OR, currently holding a Staff Research Scientist position in the Numerical Device Modeling Group. Roza has been working on the modeling of the quantum confinement effects, tunneling, high-k scattering, and the effect of stress in nanoscale devices. She is the author or co-author of 48 journal papers and conference proceedings. Roza holds more than 10 patents on various device/material novel ideas. Her research activities and interests include NEGF quantum transport modeling and atomistic modeling approaches in scaled MOSFET devices. Roza is a APS Member, and a Senior IEEE Member.